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NOTICE OF ALLOWANCE AND ISSUE FEE DUE

25M1/0422

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APPLICATION NO.	FLING DATE	TOTAL CLAIMS	EXAMINER AND GROUP ART UNIT	DATE MAILED
08/699,490	08/19/96	018	TRAN, A 2511	04/22/97
First Named Applicant	FAZIO, ALBERT			

TITLE OF INVENTION: FLASH MEMORY DEVICE CAPABLE OF SENSING A THRESHOLD VOLTAGE OF MEMORY CELLS IN A PAGE MODE OF OPERATION

ATTY'S DOCKET NO.	CLASS-SUBCLASS	BATCH NO.	APPLN. TYPE	SMALL ENTITY	FEE DUE	DATE DUE
2	042390.P3187	365-185.210	M13 UTILITY	NO	\$1290.00	07/22/97

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED.

THE ISSUE FEE MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED.

HOW TO RESPOND TO THIS NOTICE:

- I. Review the SMALL ENTITY status shown above.
If the SMALL ENTITY is shown as yes, verify your current SMALL ENTITY status:
 - A. If the status is changed, pay twice the amount of the FEE DUE shown and notify the Patent and Trademark Office of the change in status, or
 - B. If the status is the same, pay the FEE DUE shown above.
- If the SMALL ENTITY is shown as NO:
 - A. Pay FEE DUE shown above, or
 - B. File verified statement of Small Entity Status before, or with, payment of 1/2 the FEE DUE shown above.
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- III. All communications regarding this application must give application number and batch number. Please direct all communication prior to issuance to Box ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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An Examiner's Amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 C.F.R. § 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the Issue Fee.

In the title :

Change the title to read :

--Flash memory device capable of sensing a threshold voltage of memory cells in a page mode of operation--.

In the claims :

In claim 14, line 4, change "sense" to --sensing--;

line 6, change "sense" to --sensing--;

In claim 18, line 2, add --voltage-- after "threshold"; and

line 2, change "strong" to --storing--.

The above changes are to remedy antecedent basis deficiencies.

The following is an examiner's statement of reasons for allowance:

The prior art, alone or in combination, fails to teach or fairly suggest a memory device comprising a memory array having data stored in a memory cell, the memory cell having a select gate coupled to a wordline, a first electrode coupled to a

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bitline, and a second electrode coupled to a conductor; and a periphery circuit coupled to the memory array, the periphery circuit transmitting a first voltage to the wordline and transmitting a second voltage to the conductor such that the bitline is set to a third voltage that is equal to the first voltage minus a threshold voltage of the memory cell, wherein the periphery circuit senses the third voltage to determine the data stored by the memory cell.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

at
April 18, 1997


DAVID C. NELMS
SUPERVISORY PATENT EXAMINER
GROUP 2500